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Applicant(s): Linda J. Rankin et al.
DOMAIN PARTITIONING IN A MULTI-NODE SYSTEM

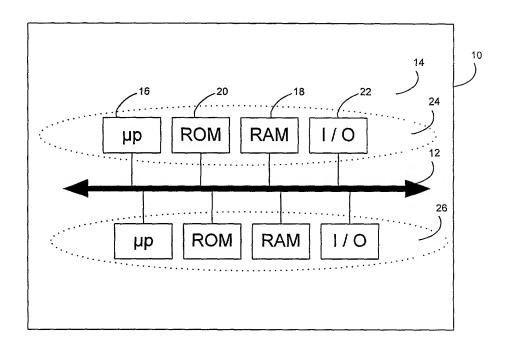


Fig. 1 (prior art)

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50 RAM μр μρ 52 Scalable Node Controller 56 Switch PCI I/O Hub Bridge Controller

Fig. 2 (prior art)

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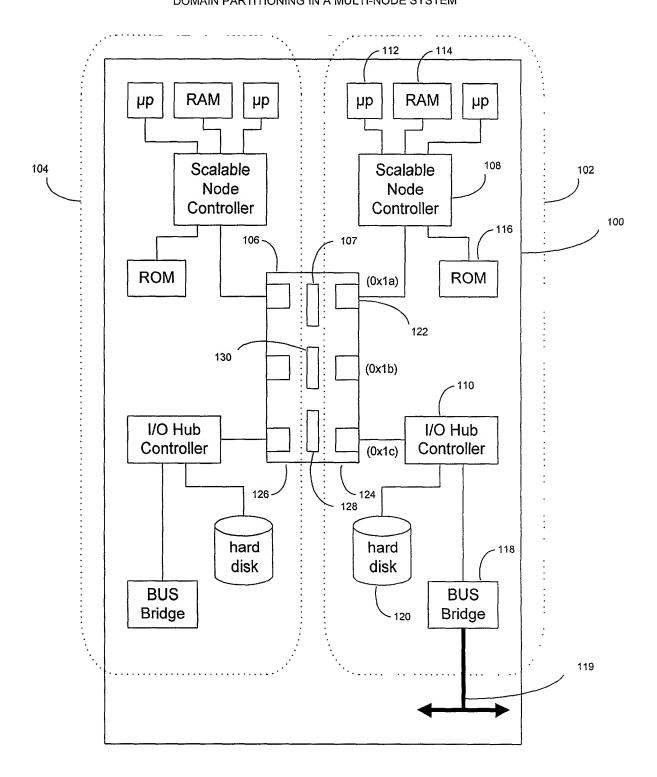
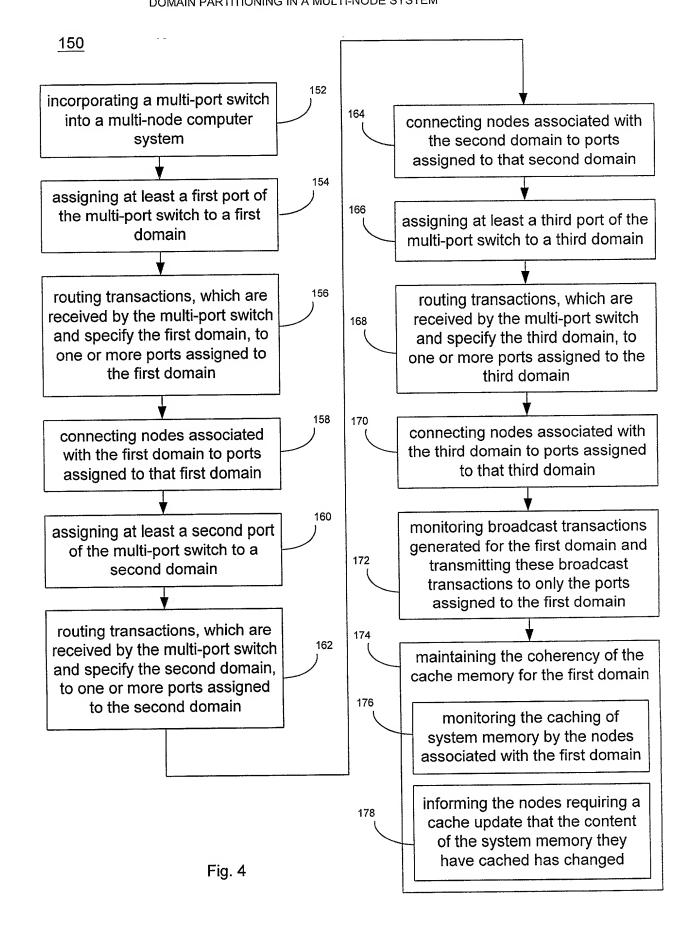
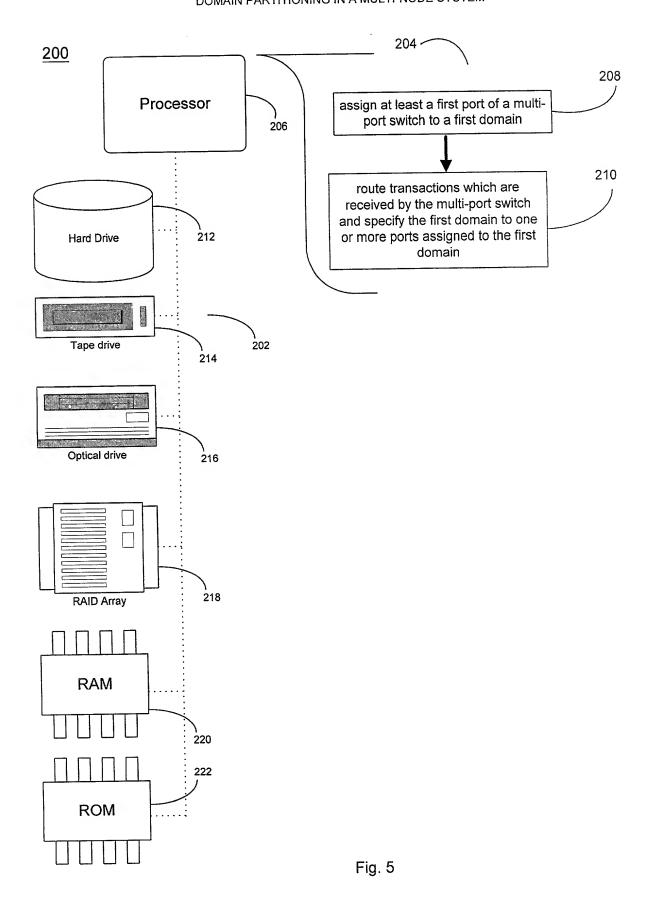


Fig. 3

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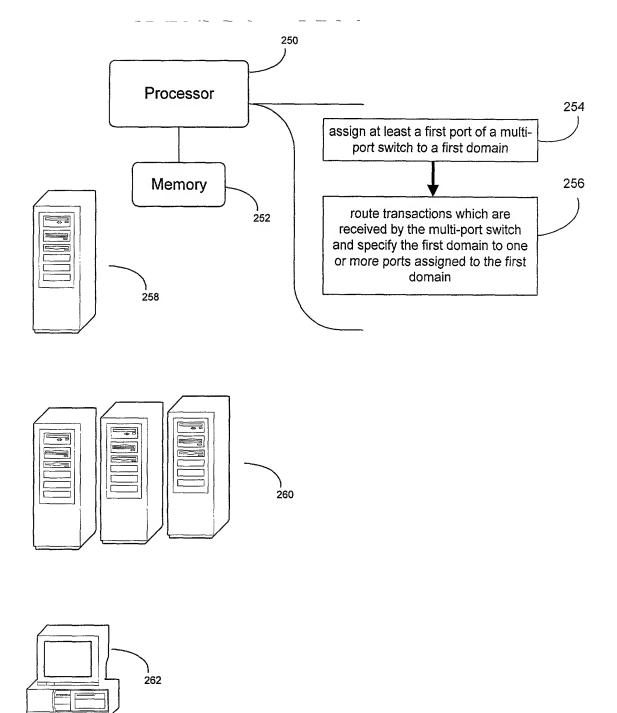


Fig. 6